

FIG. 1

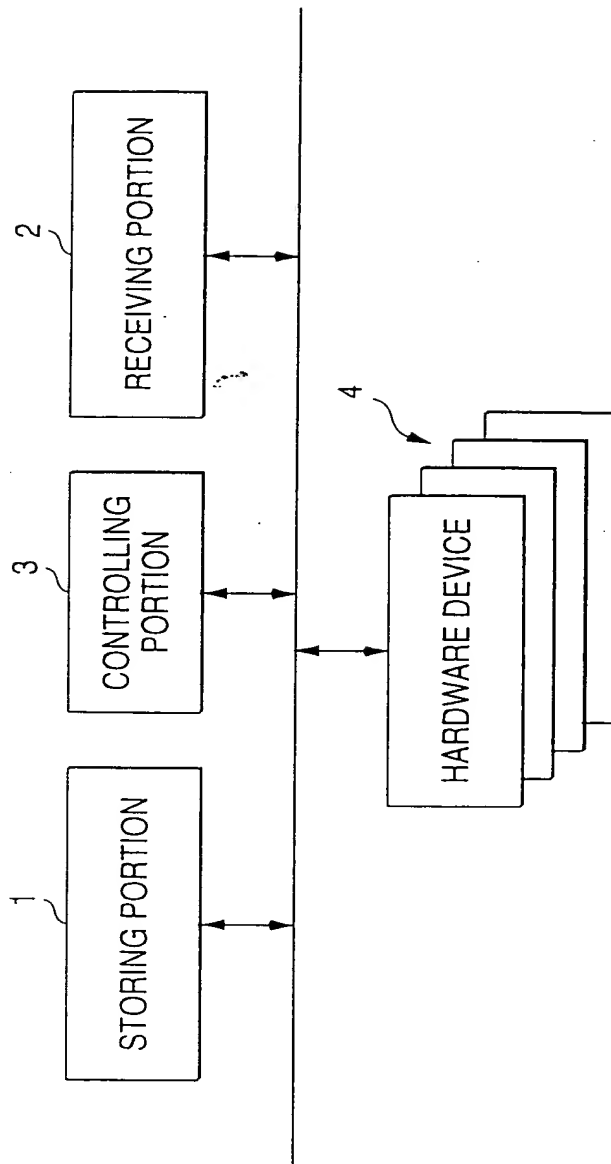


FIG. 2

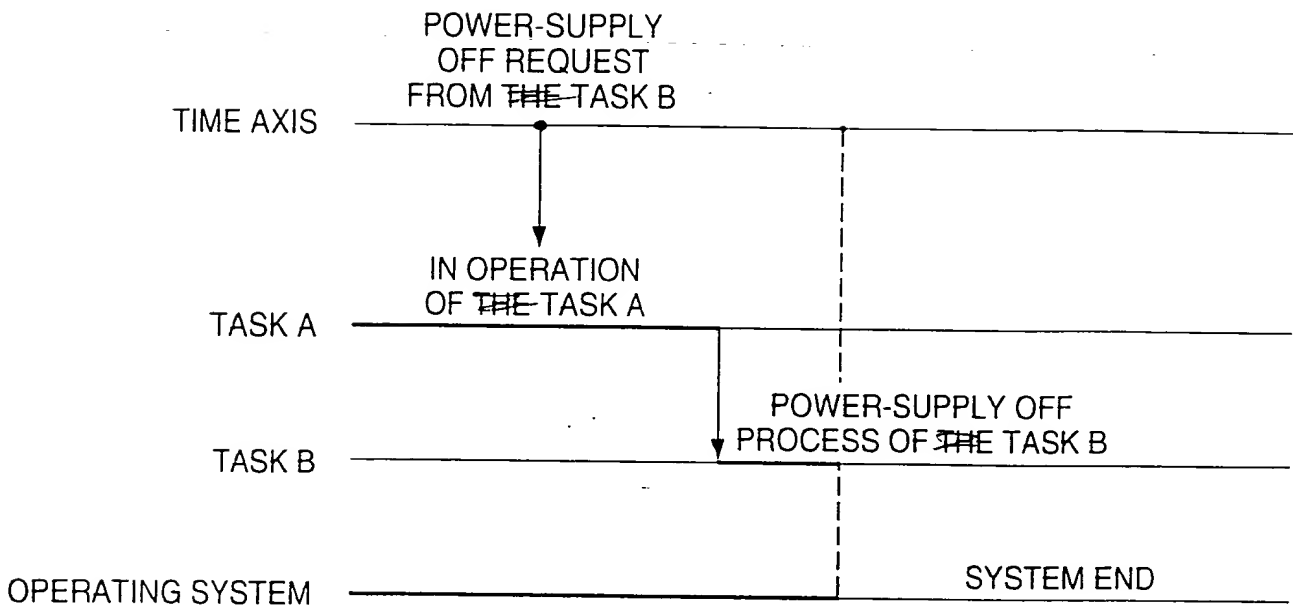


FIG. 3

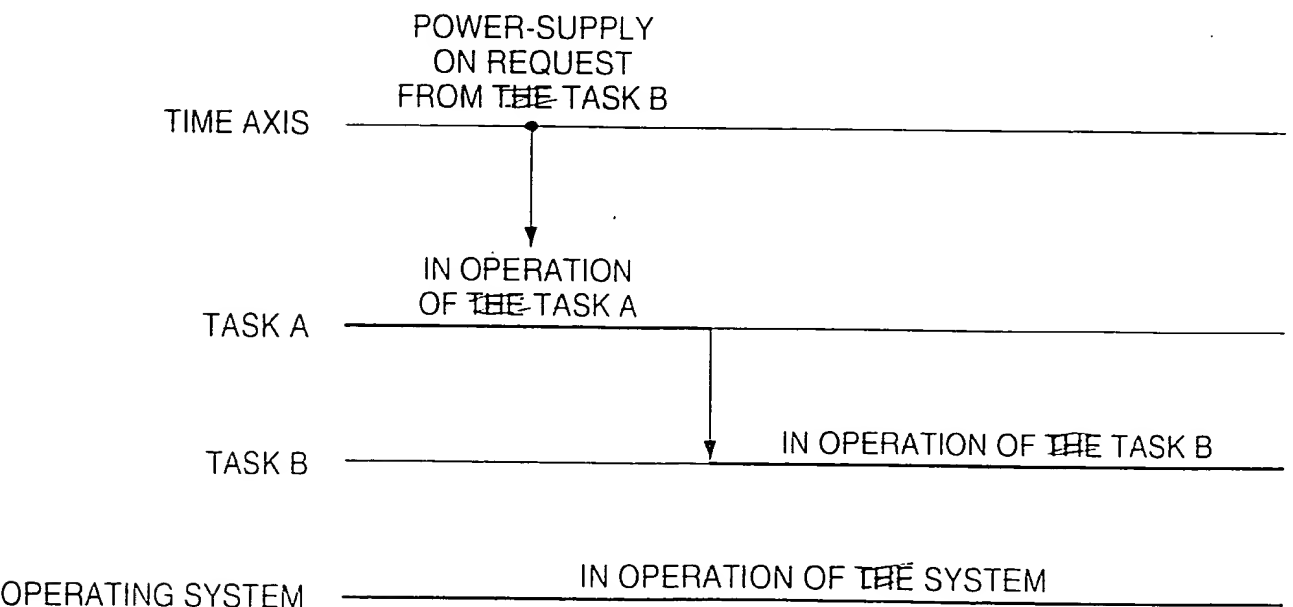


FIG. 4

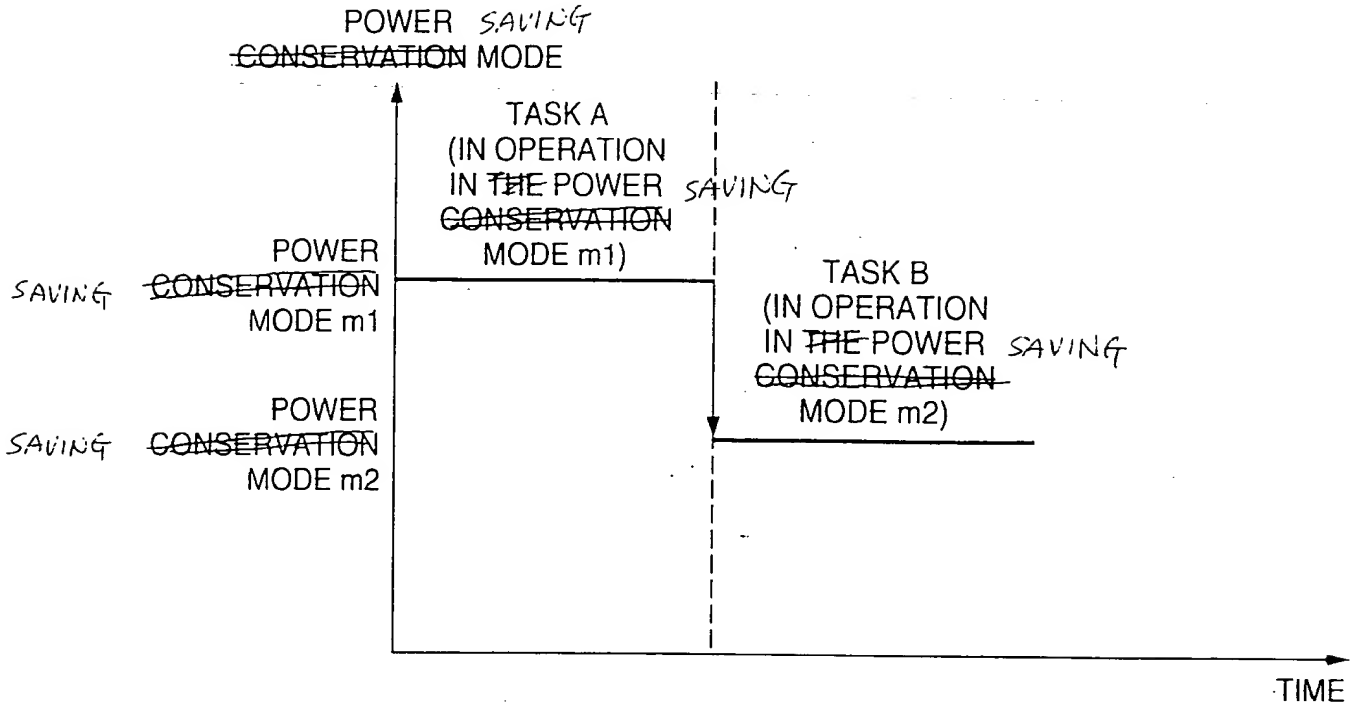


FIG. 5

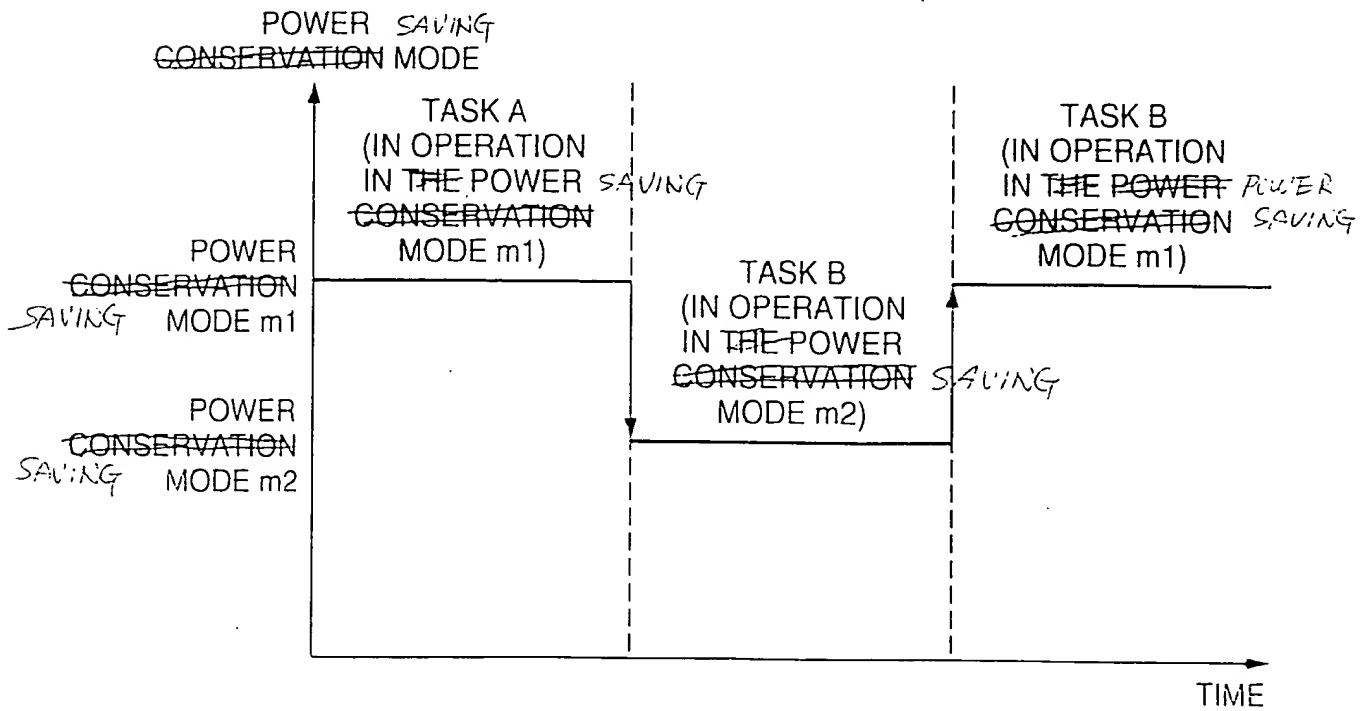


FIG. 6

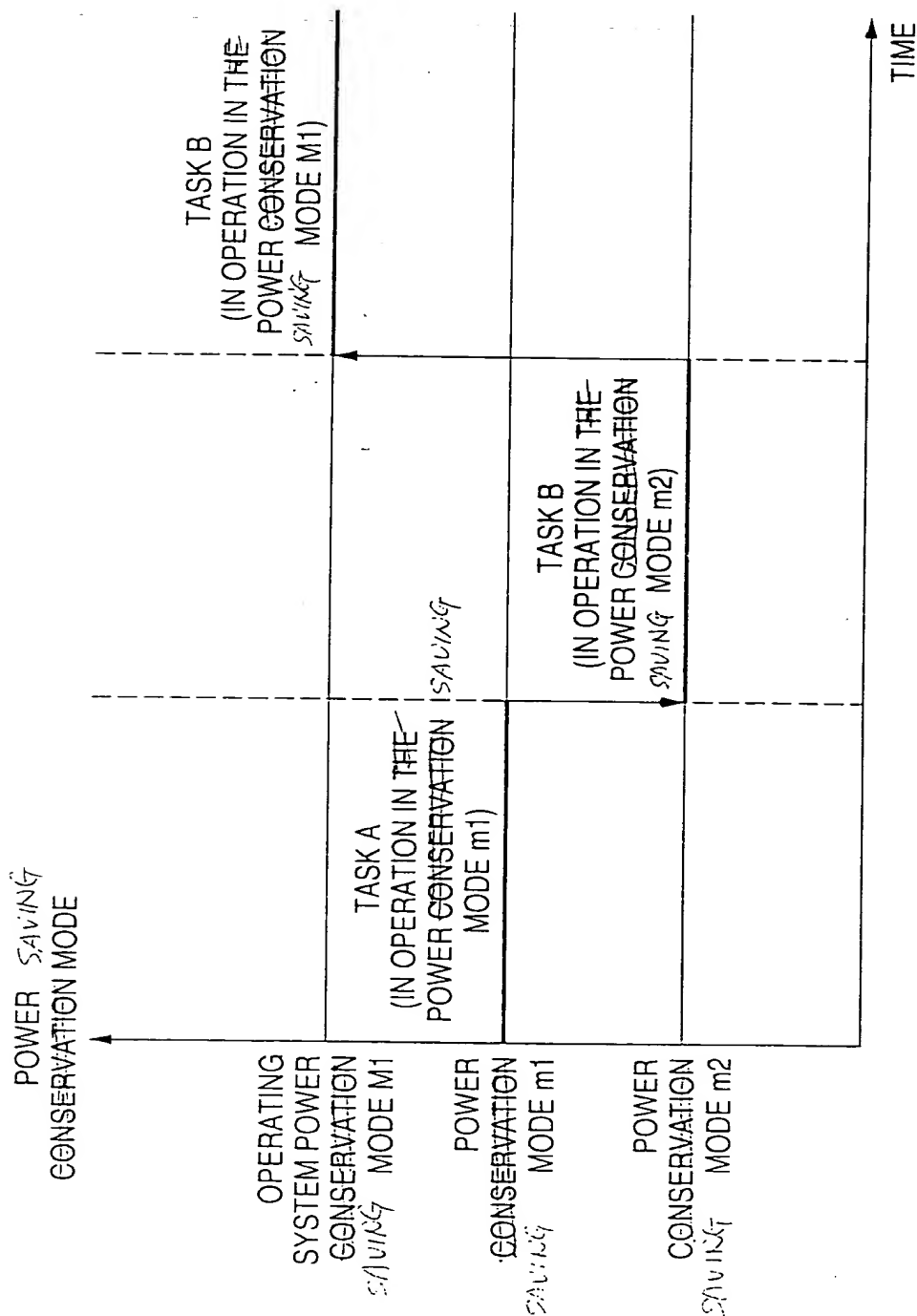
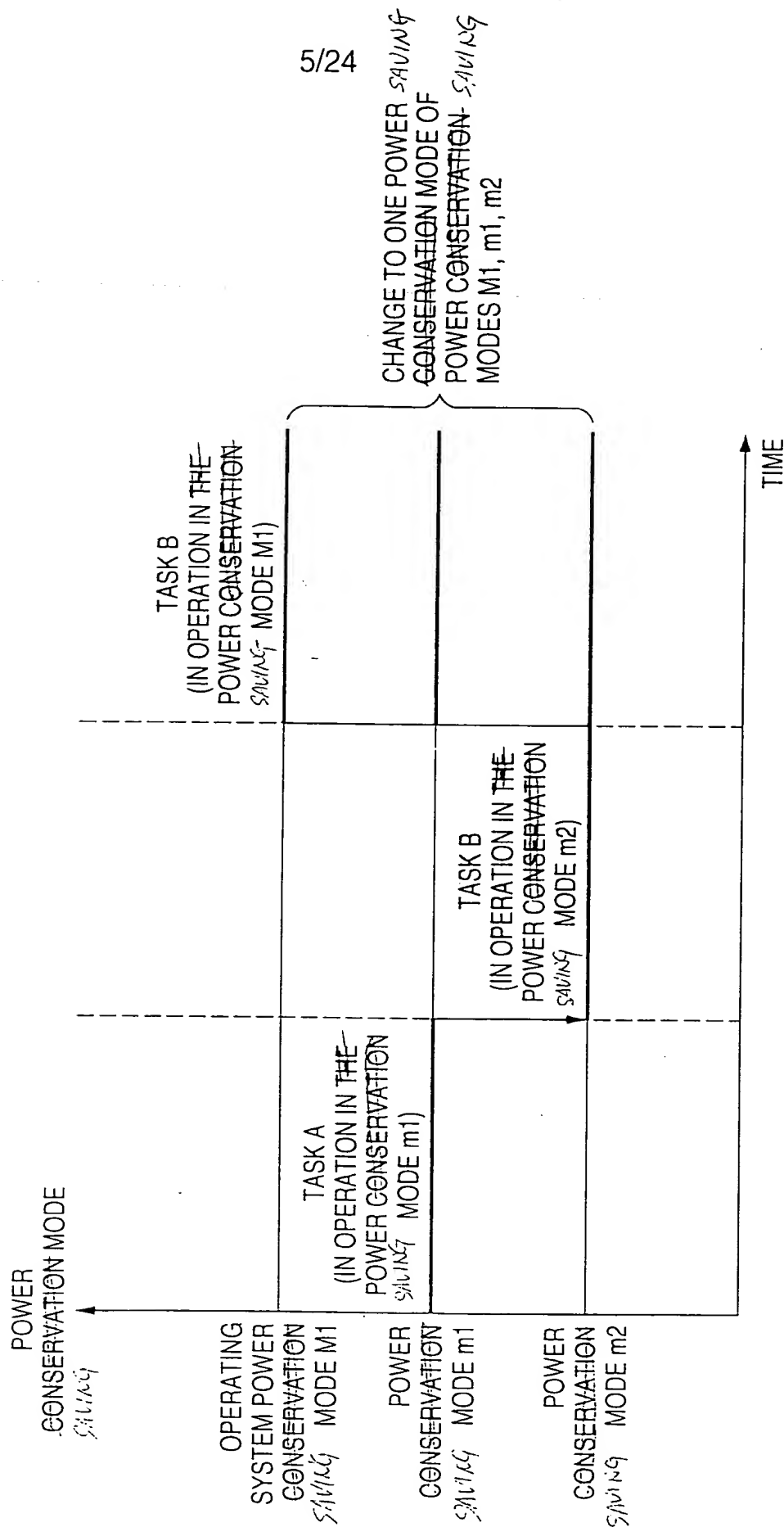


FIG. 7



PERIODIC TIMER

FIG. 8

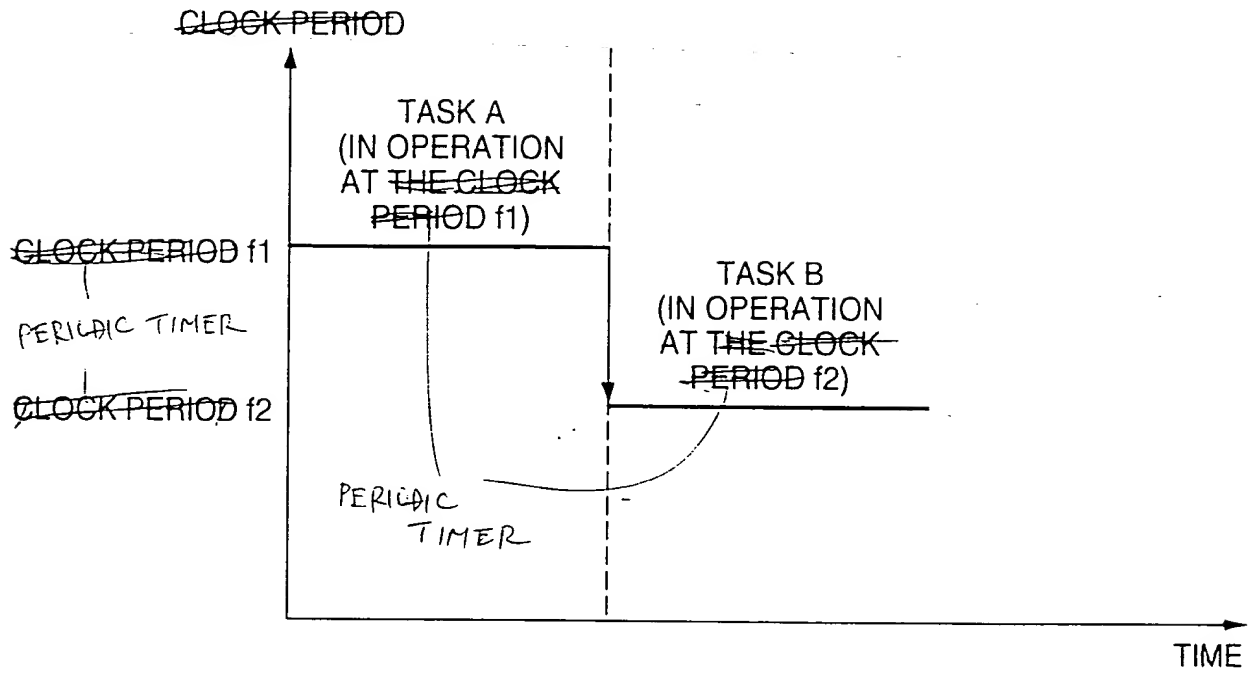


FIG. 9

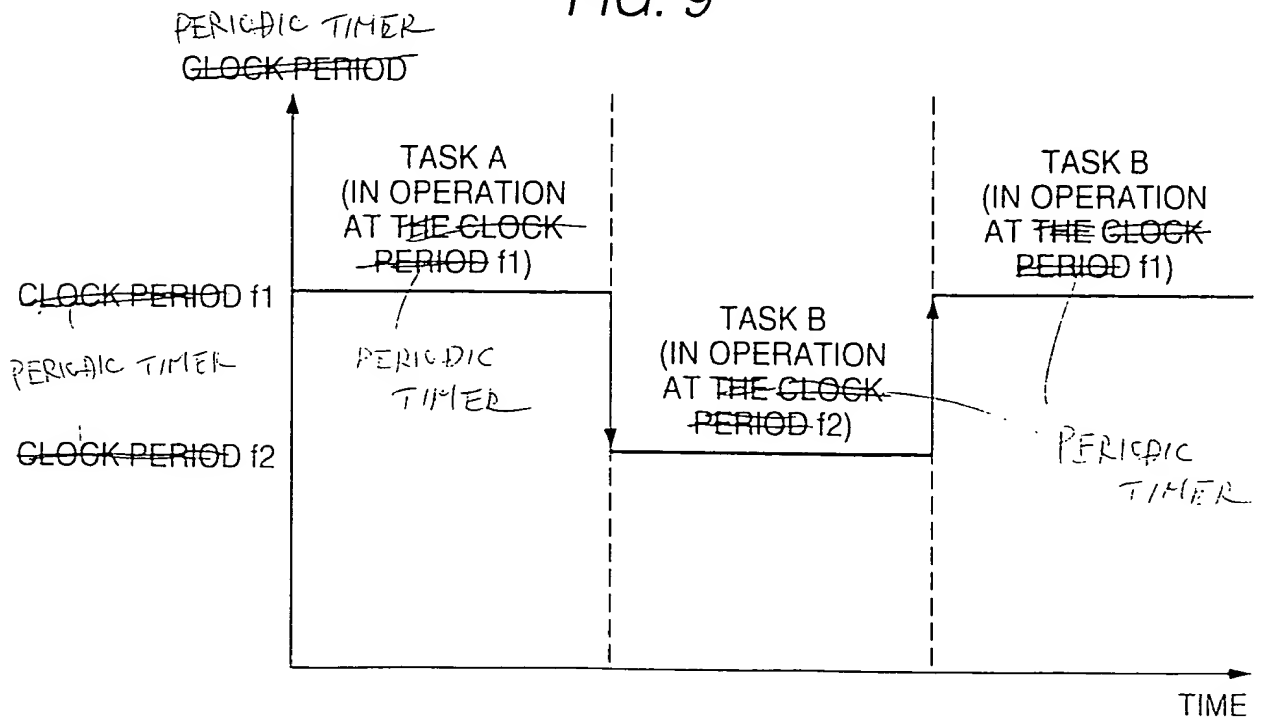


FIG. 10

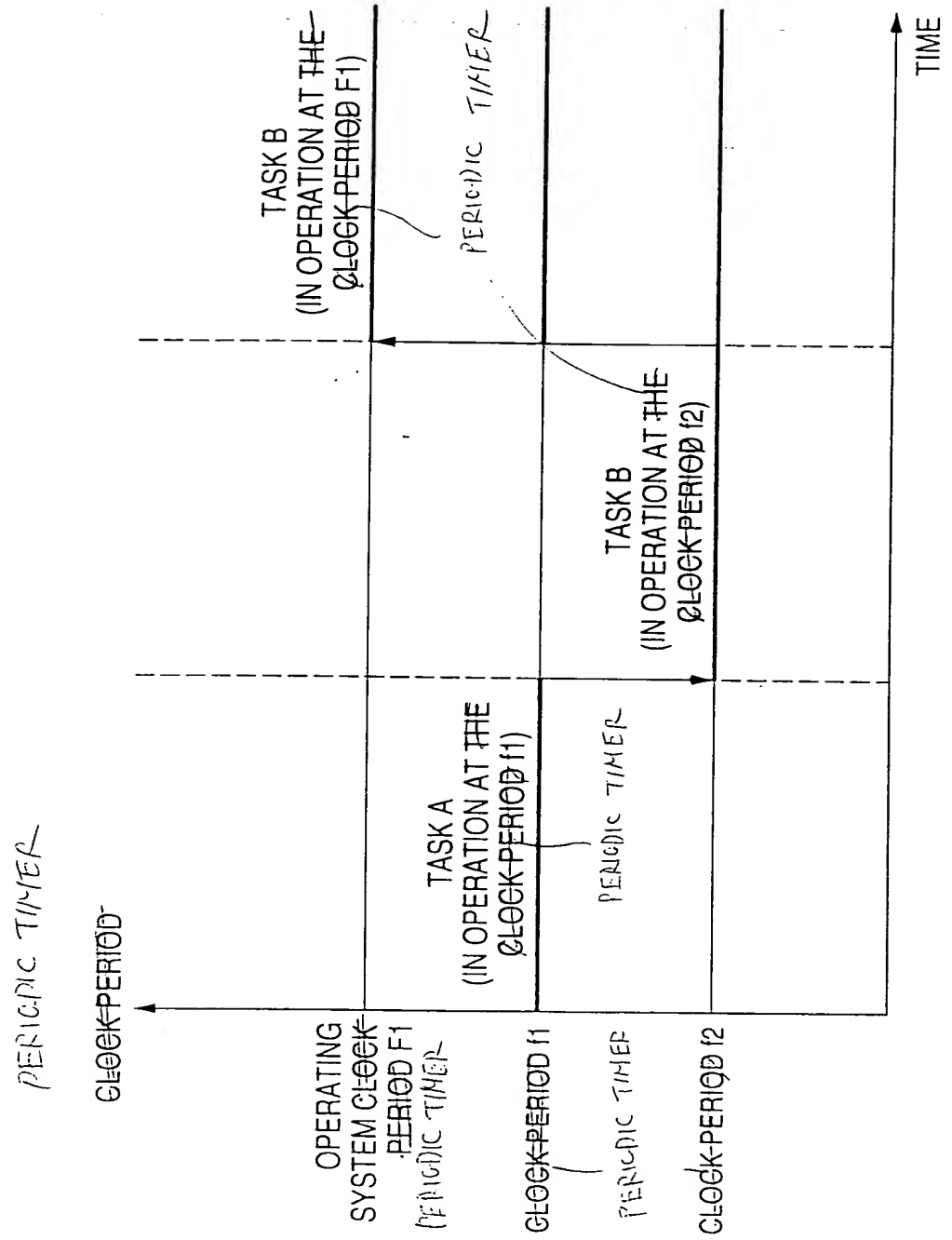


FIG. 11

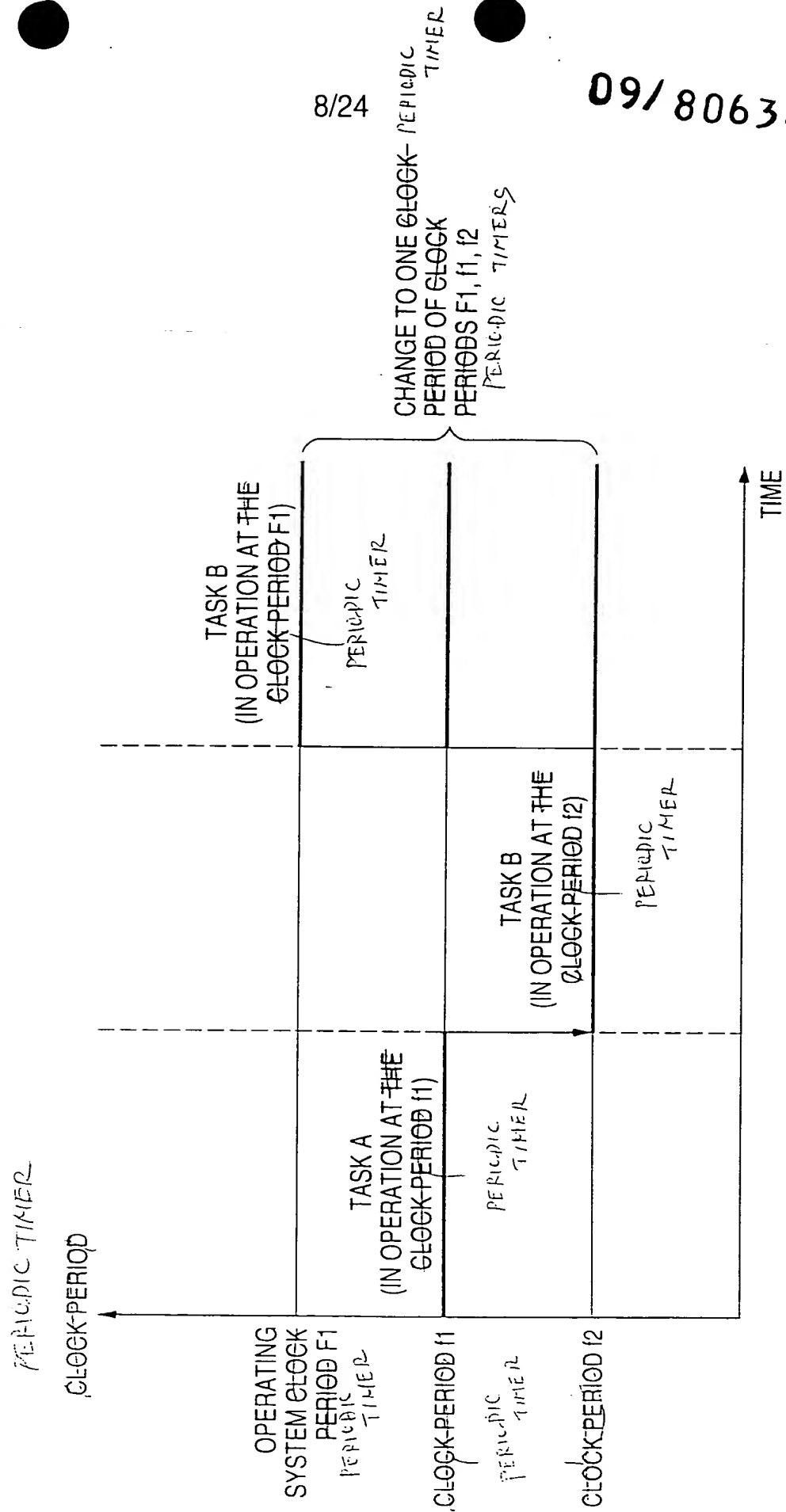
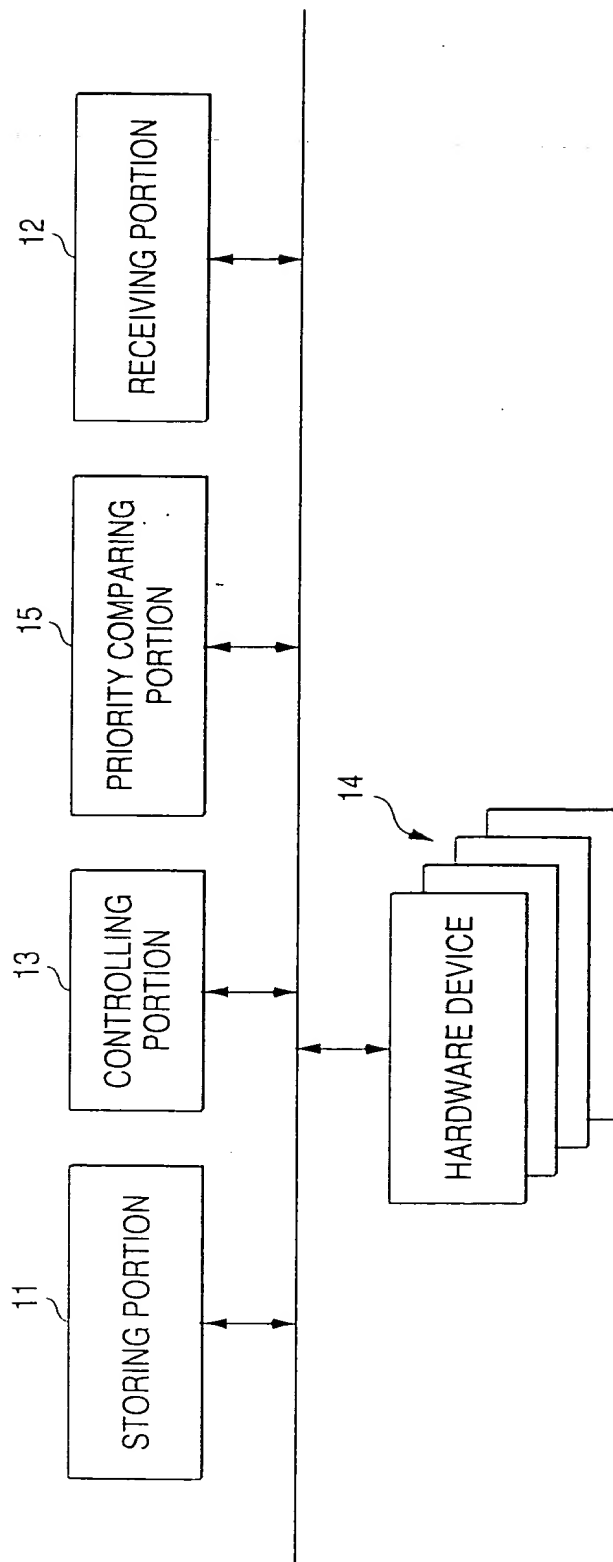




FIG. 12



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FIG. 13

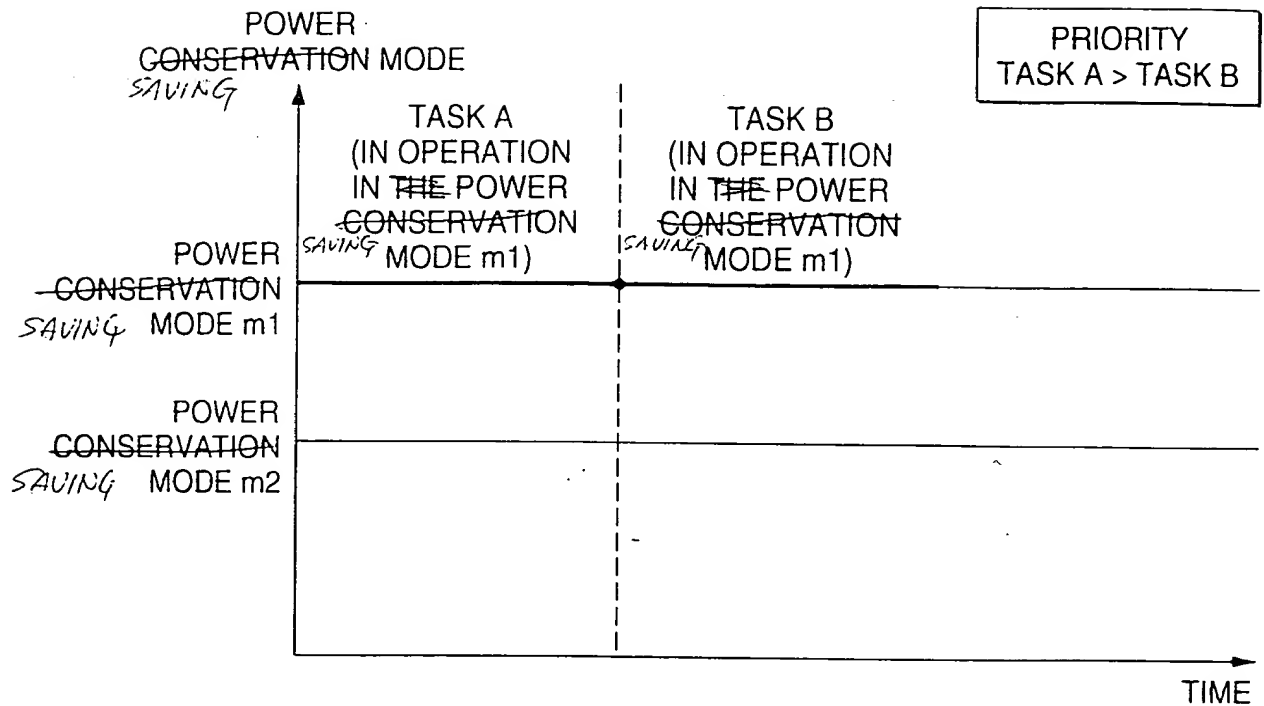
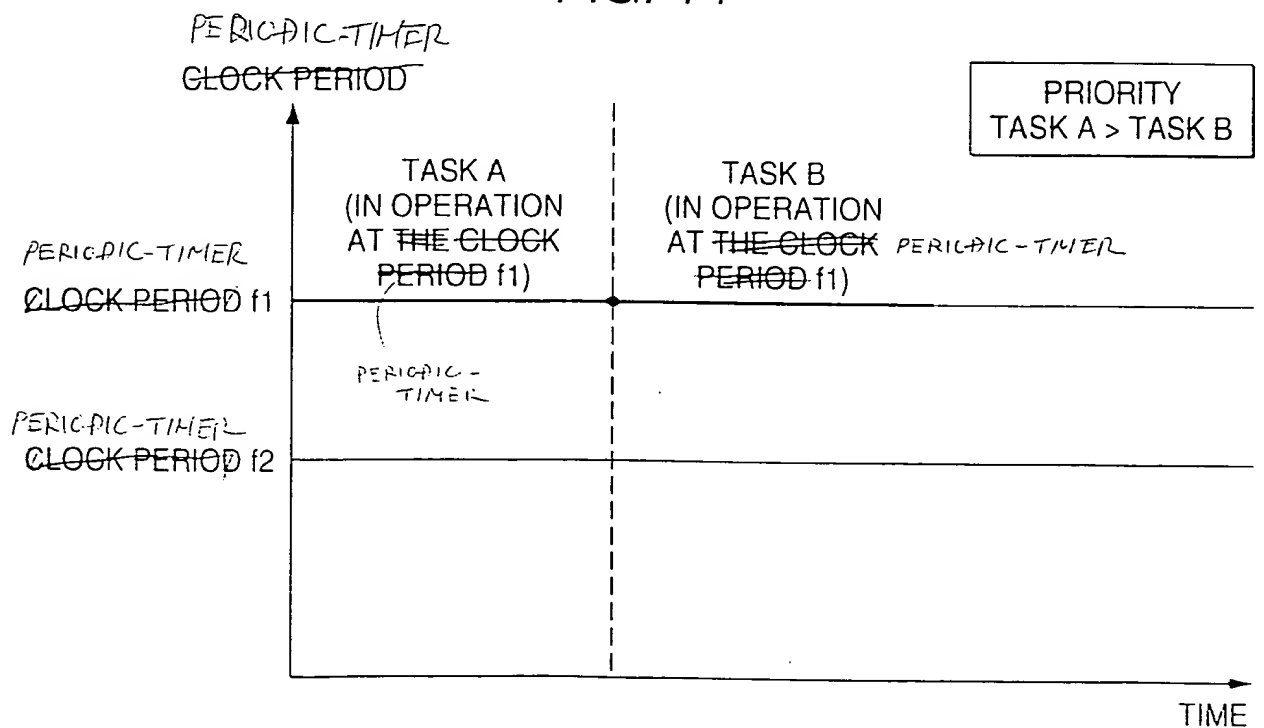
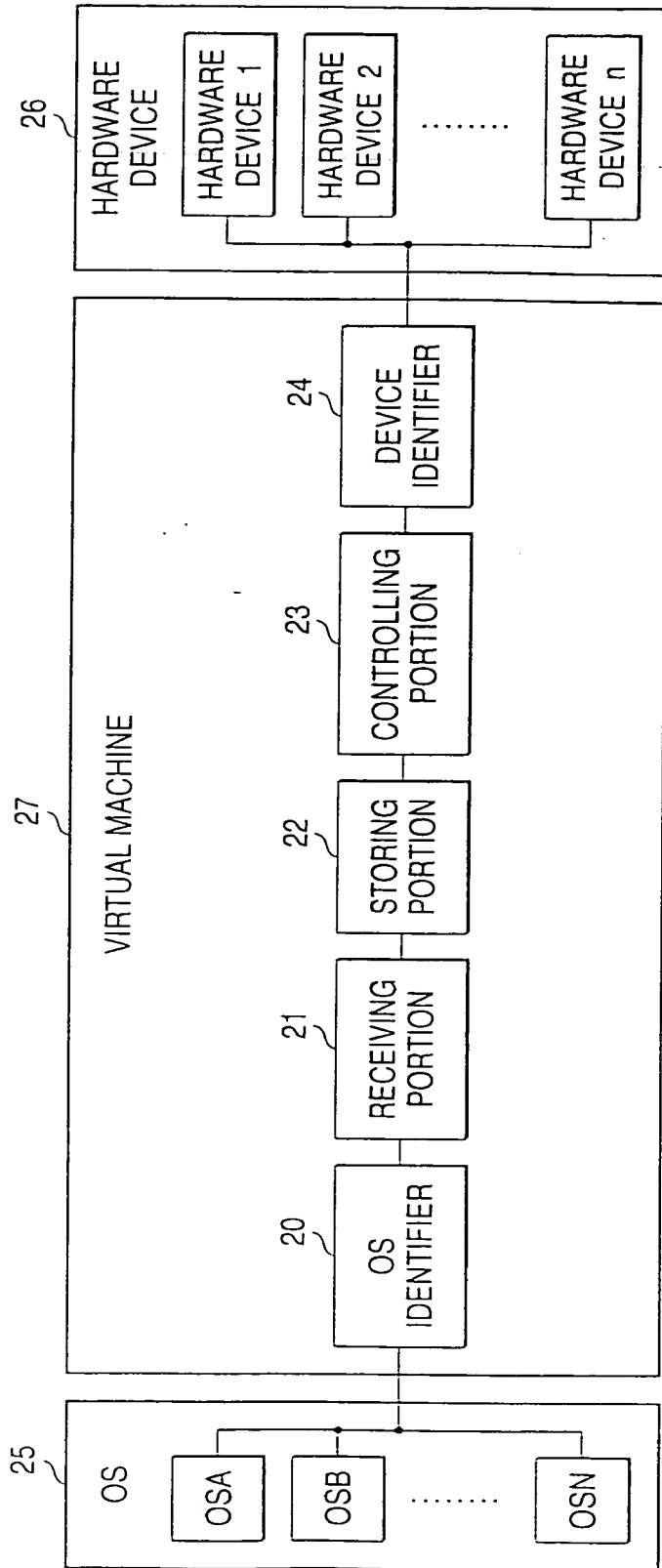


FIG. 14



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FIG. 15



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FIG. 16

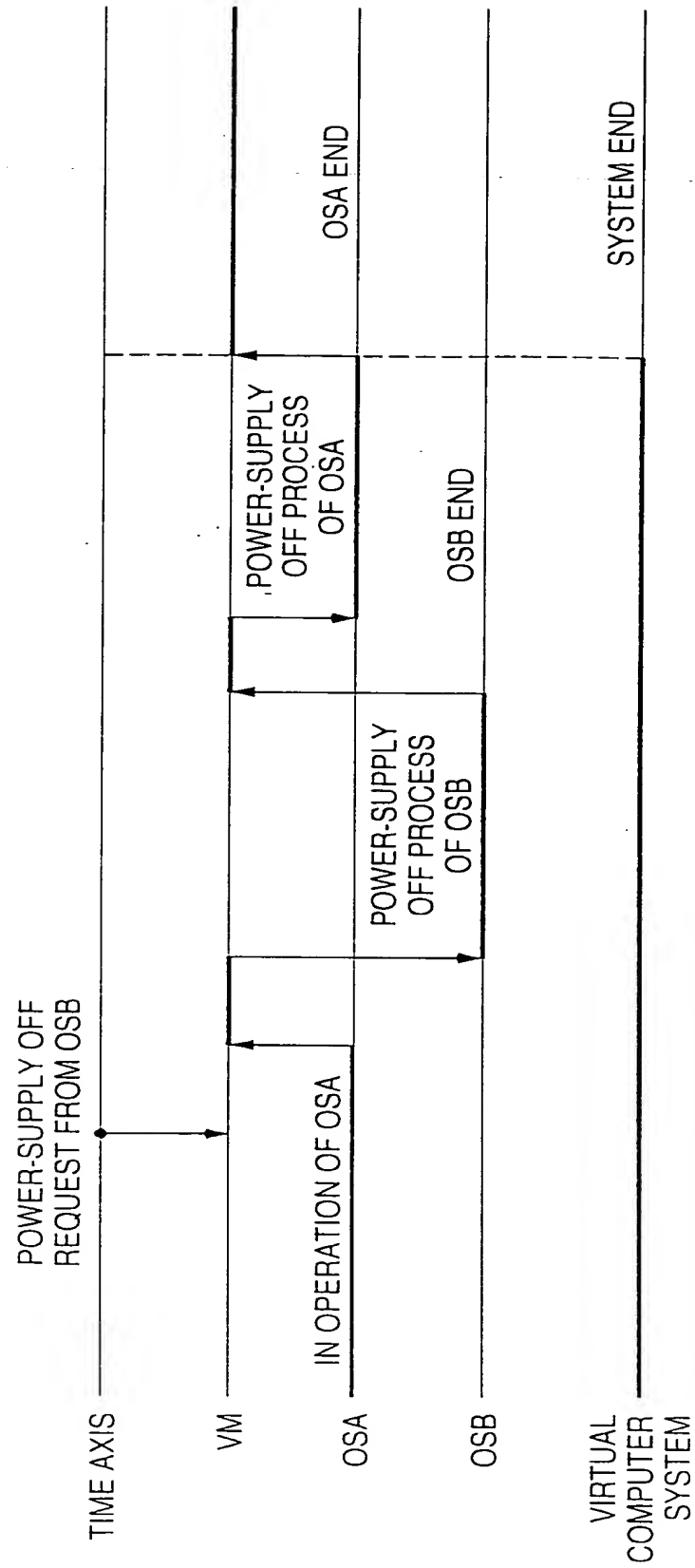
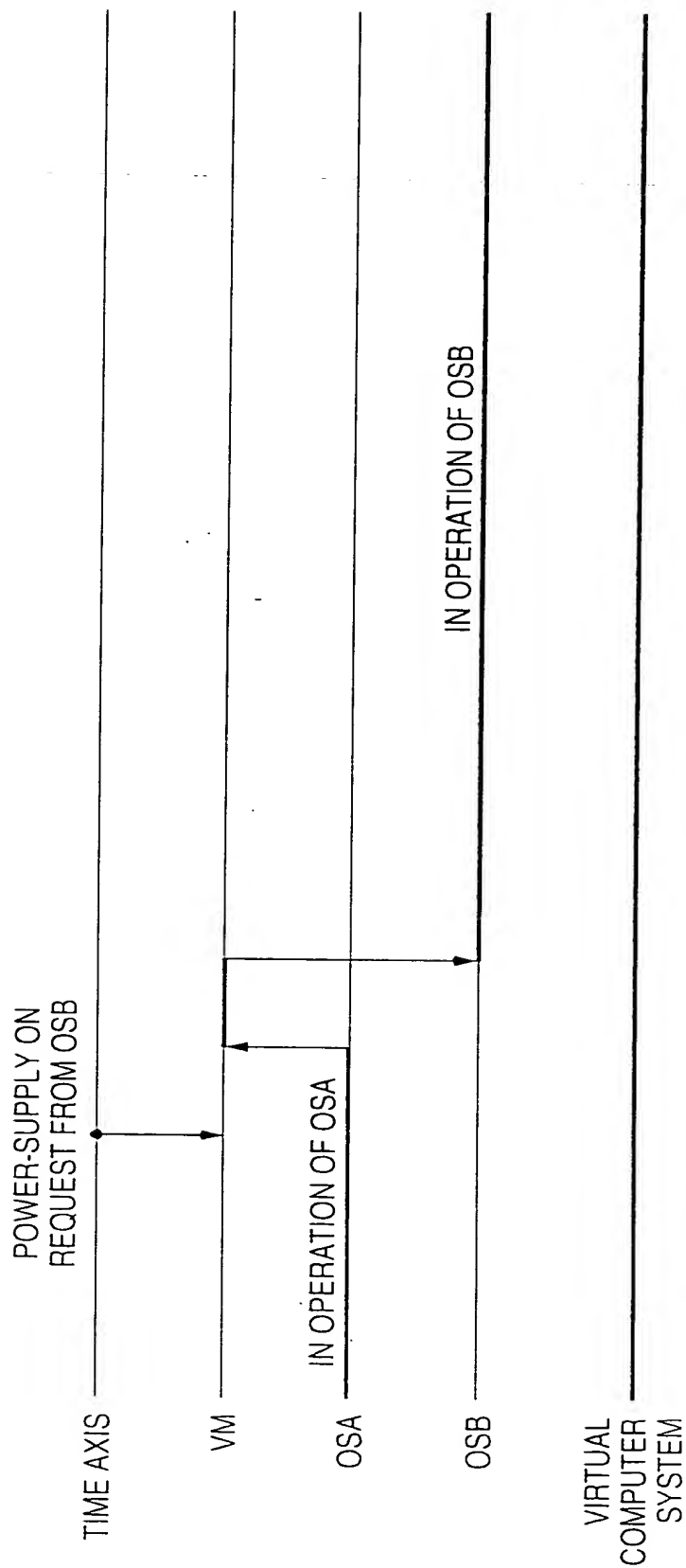


FIG. 17



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FIG. 18

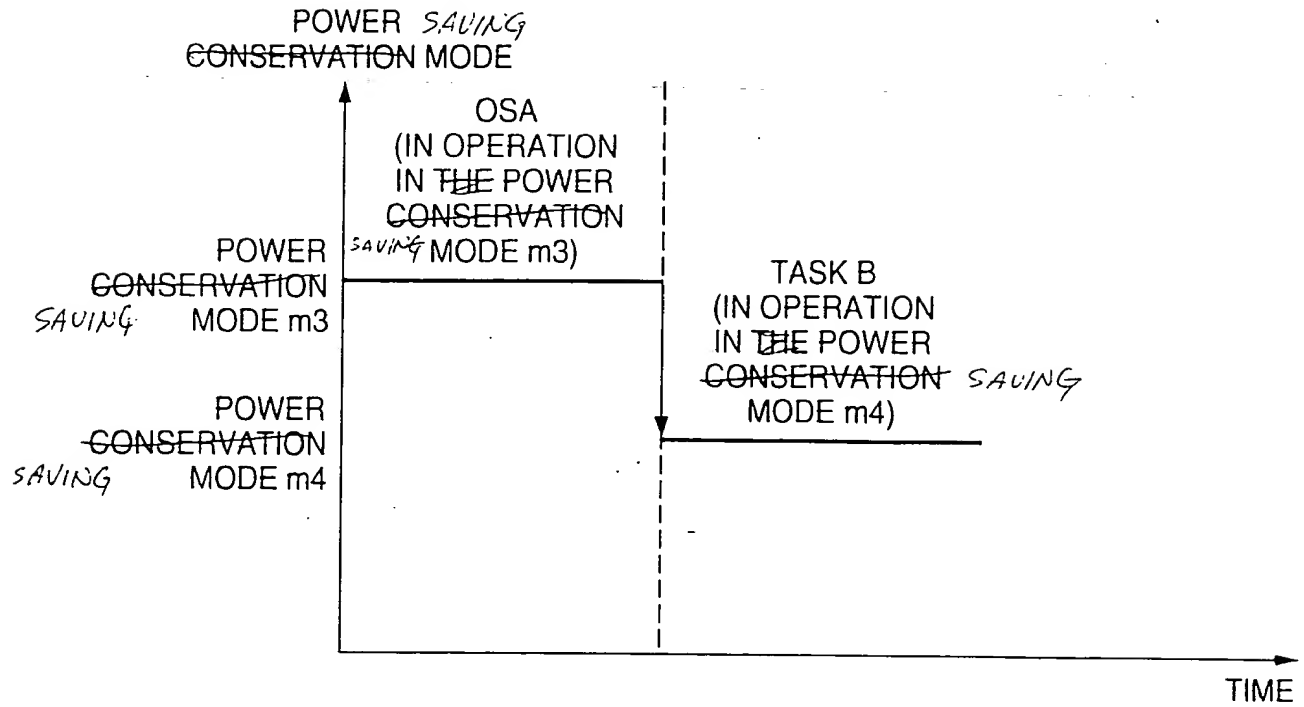


FIG. 19

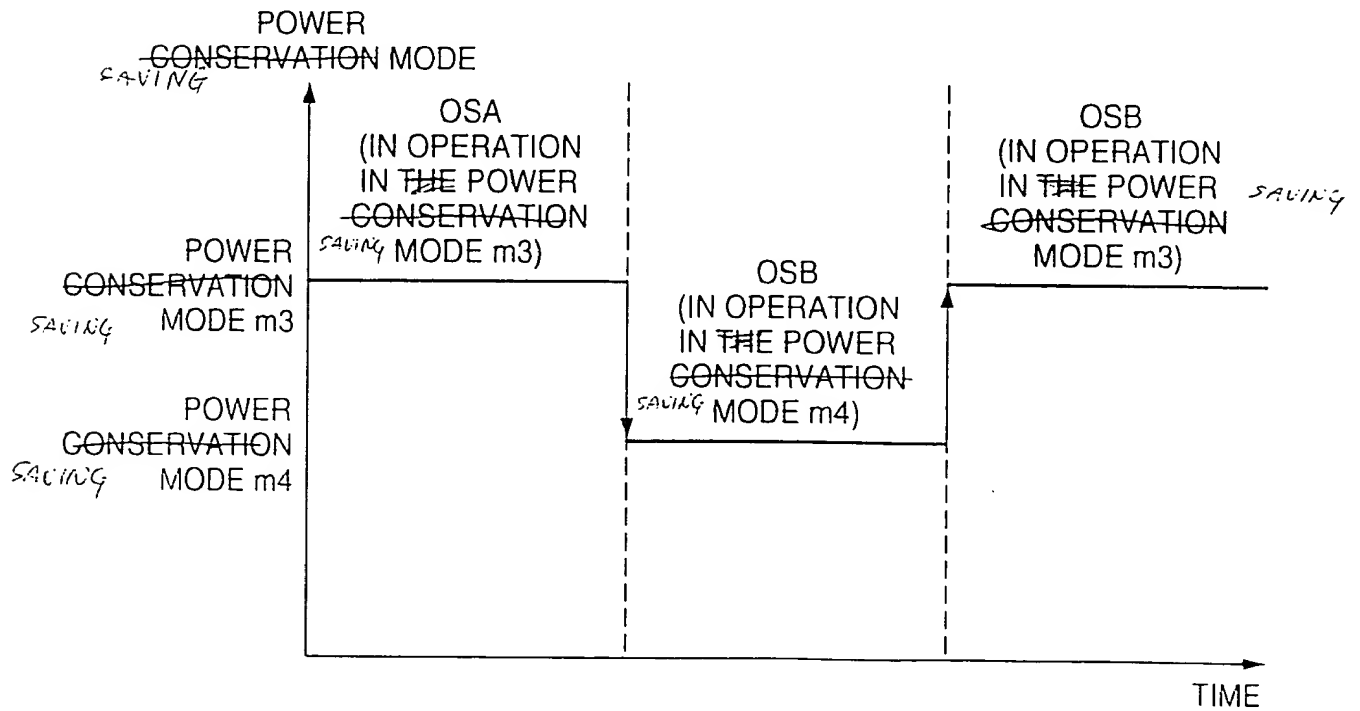


FIG. 20

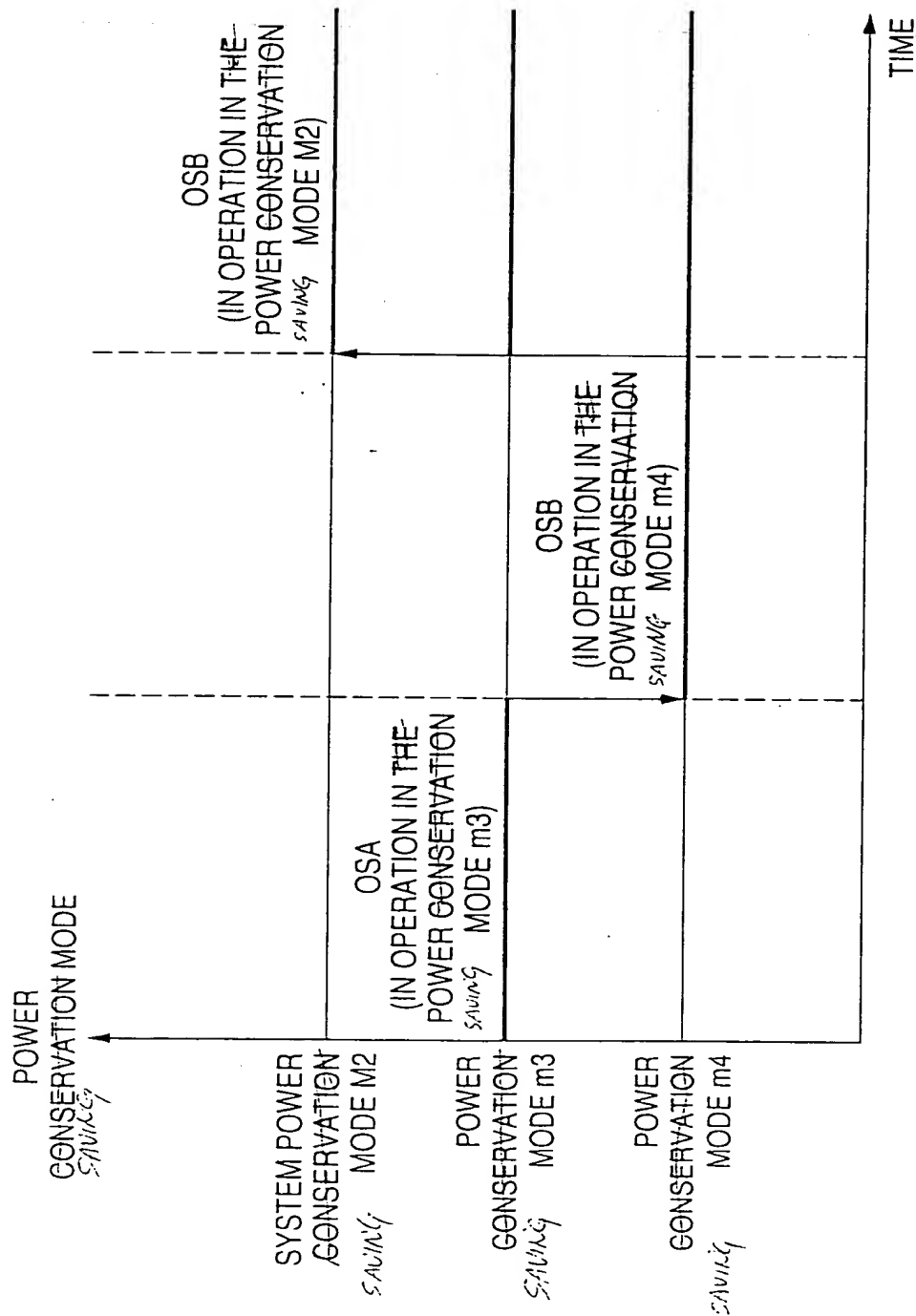


FIG. 21

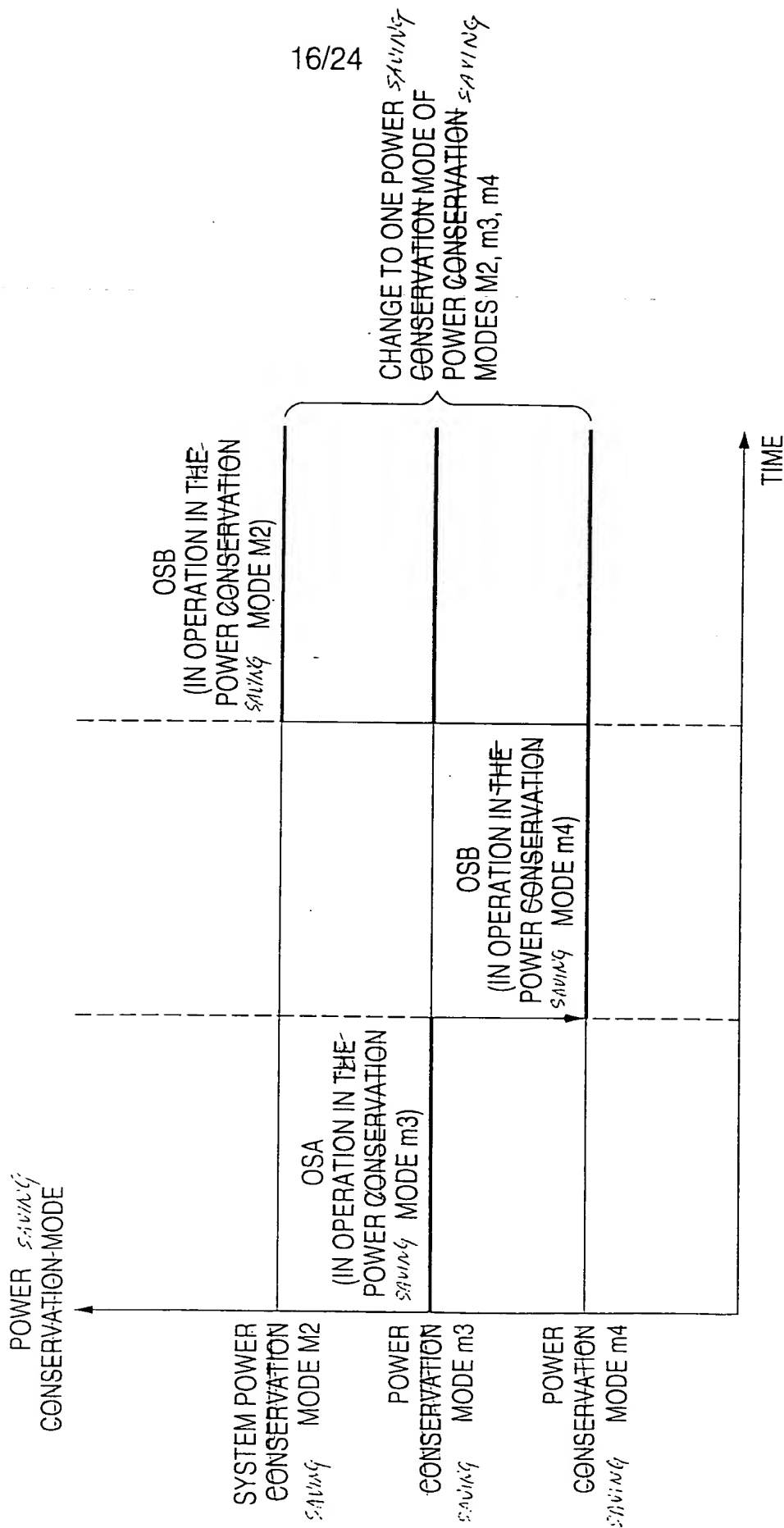




FIG. 22

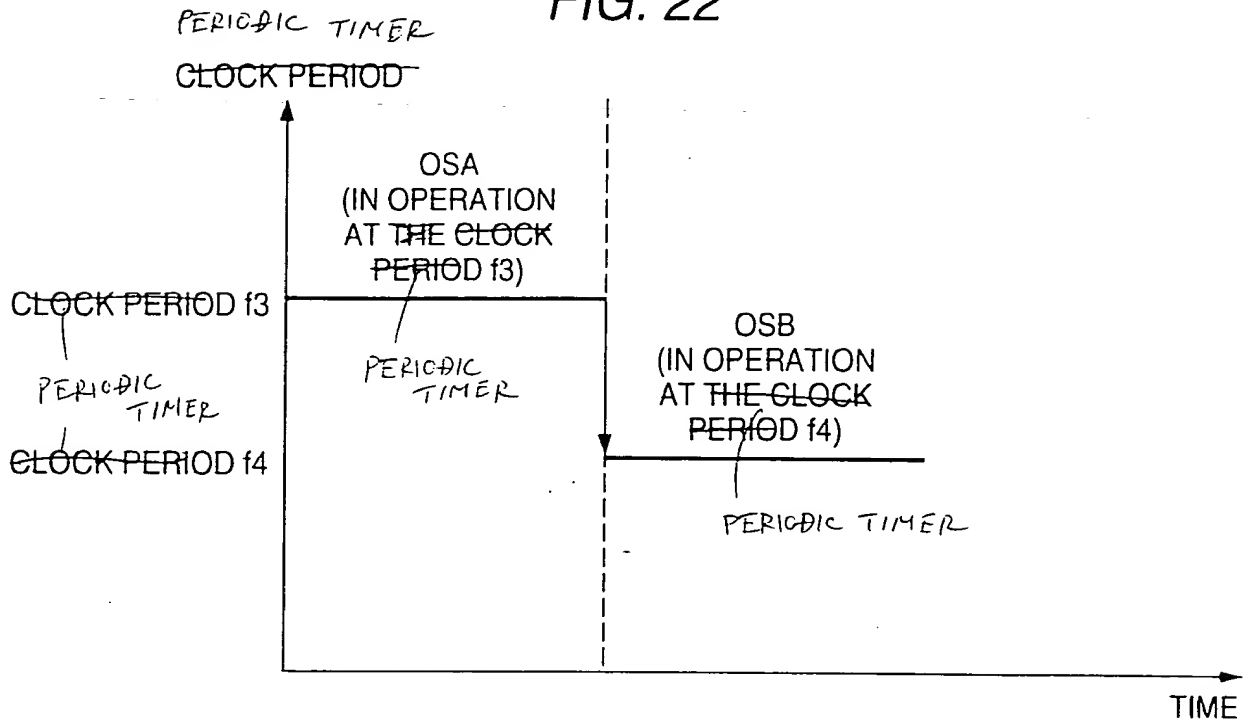


FIG. 23

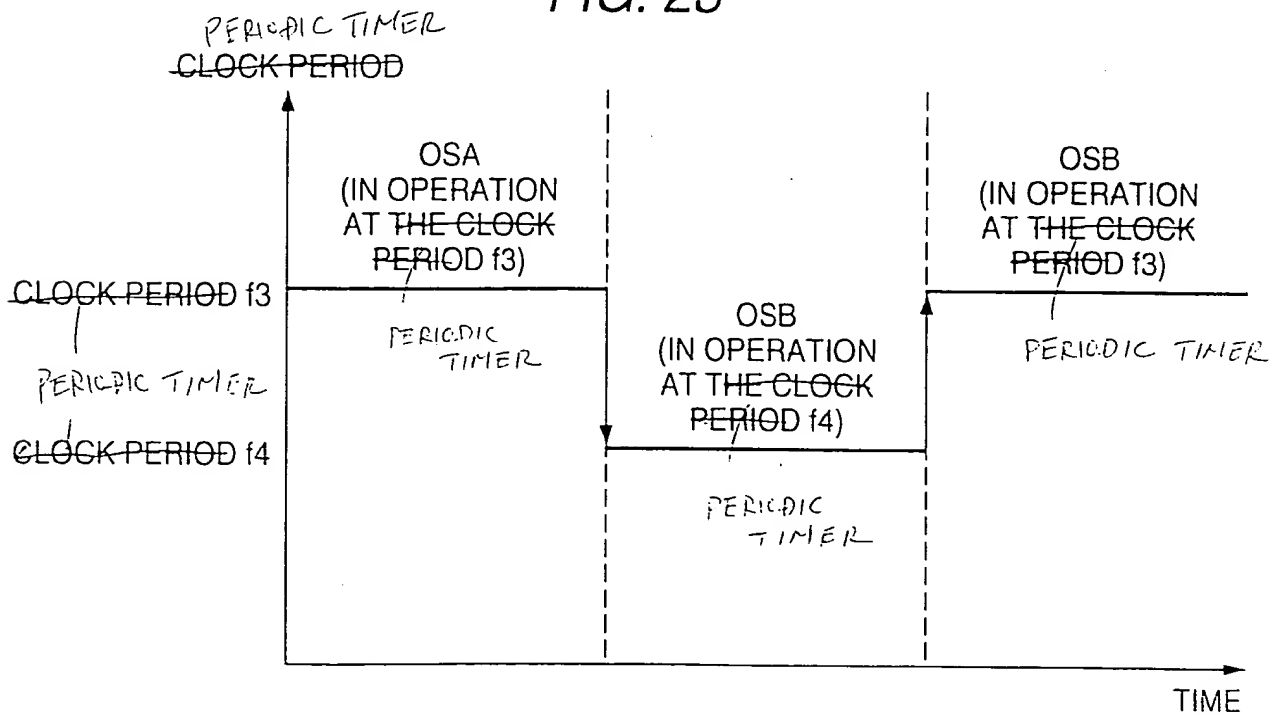
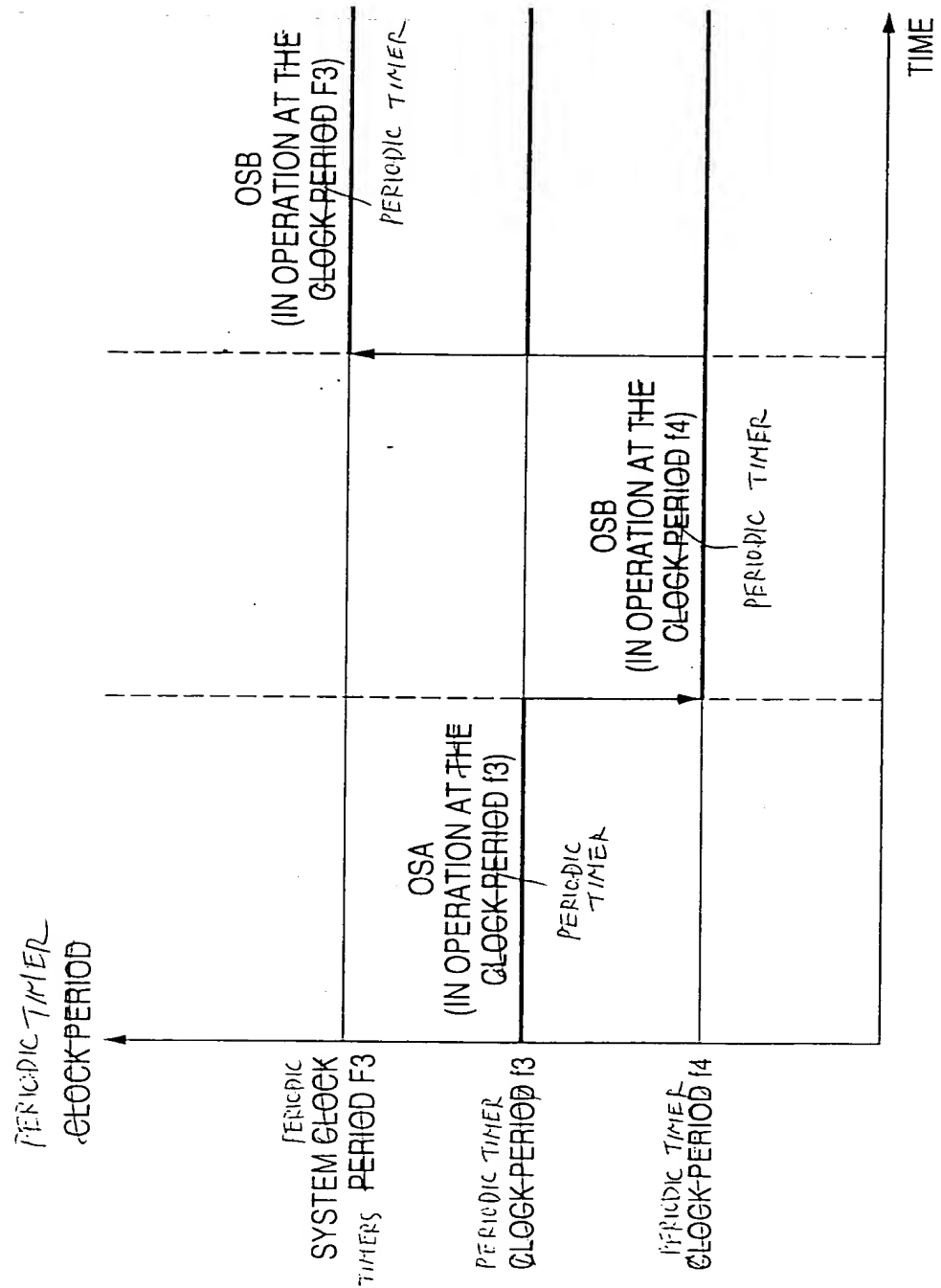


FIG. 24



PERIODIC TIME  
CLOCK PERIOD

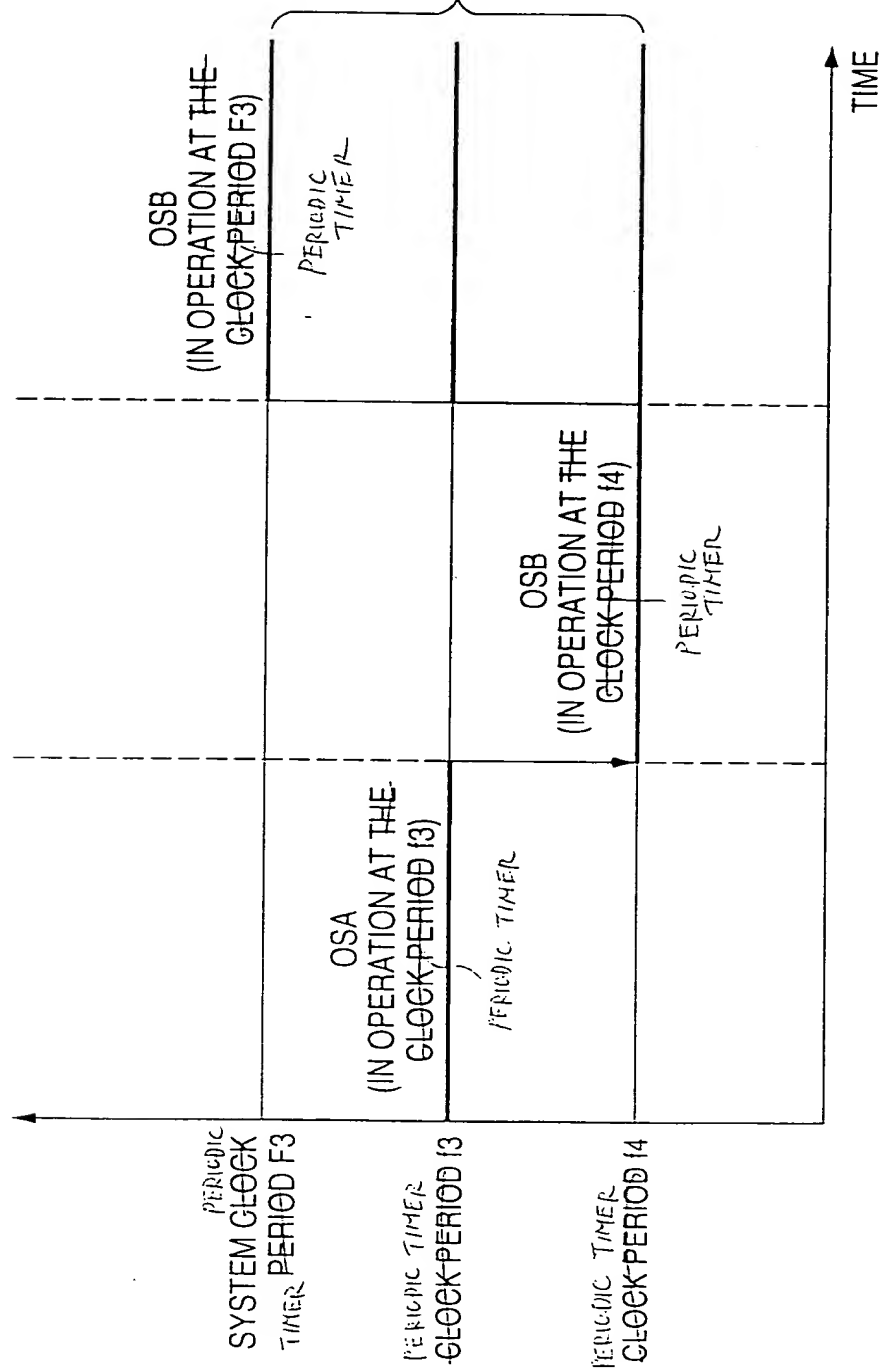


FIG. 26

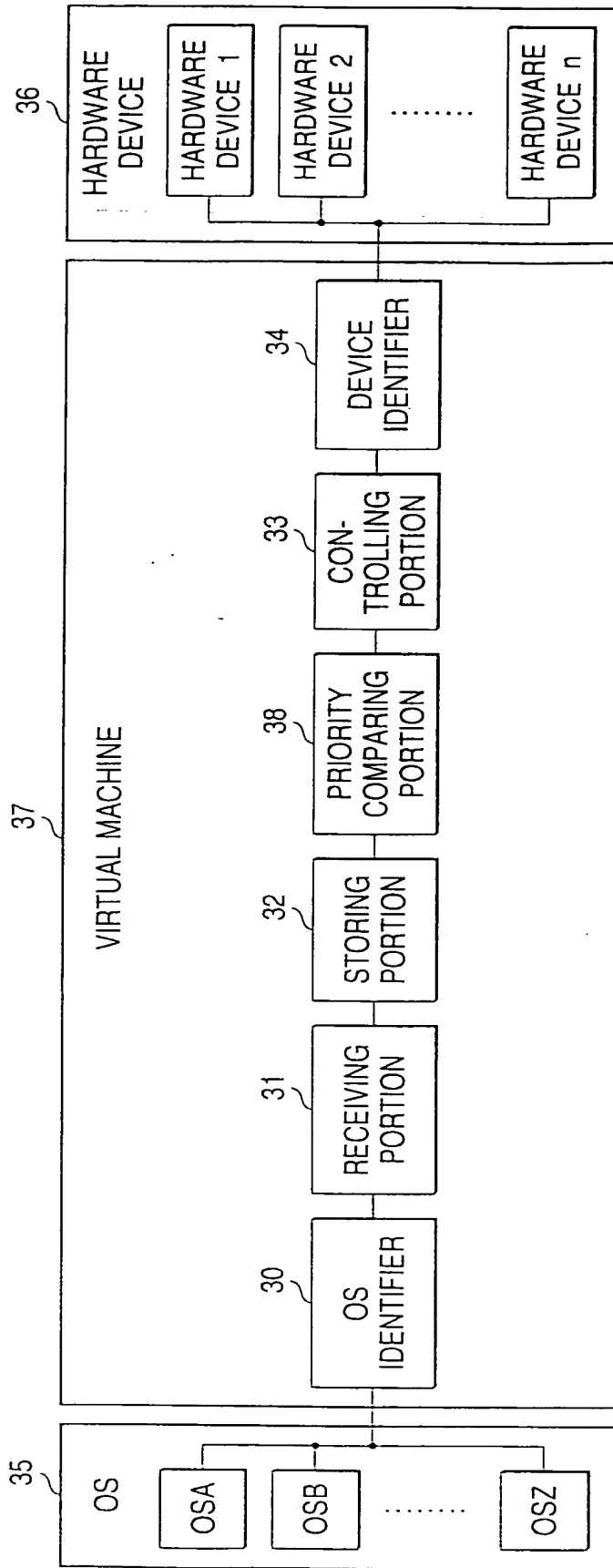


FIG. 27

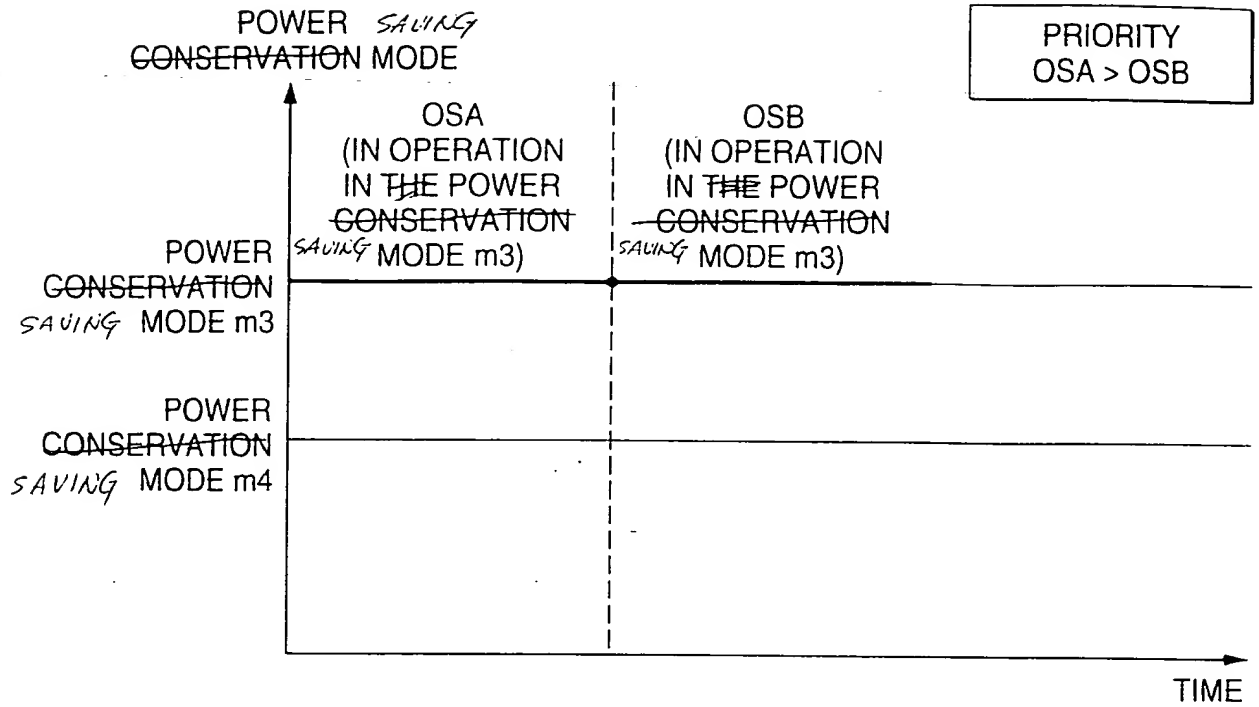


FIG. 28

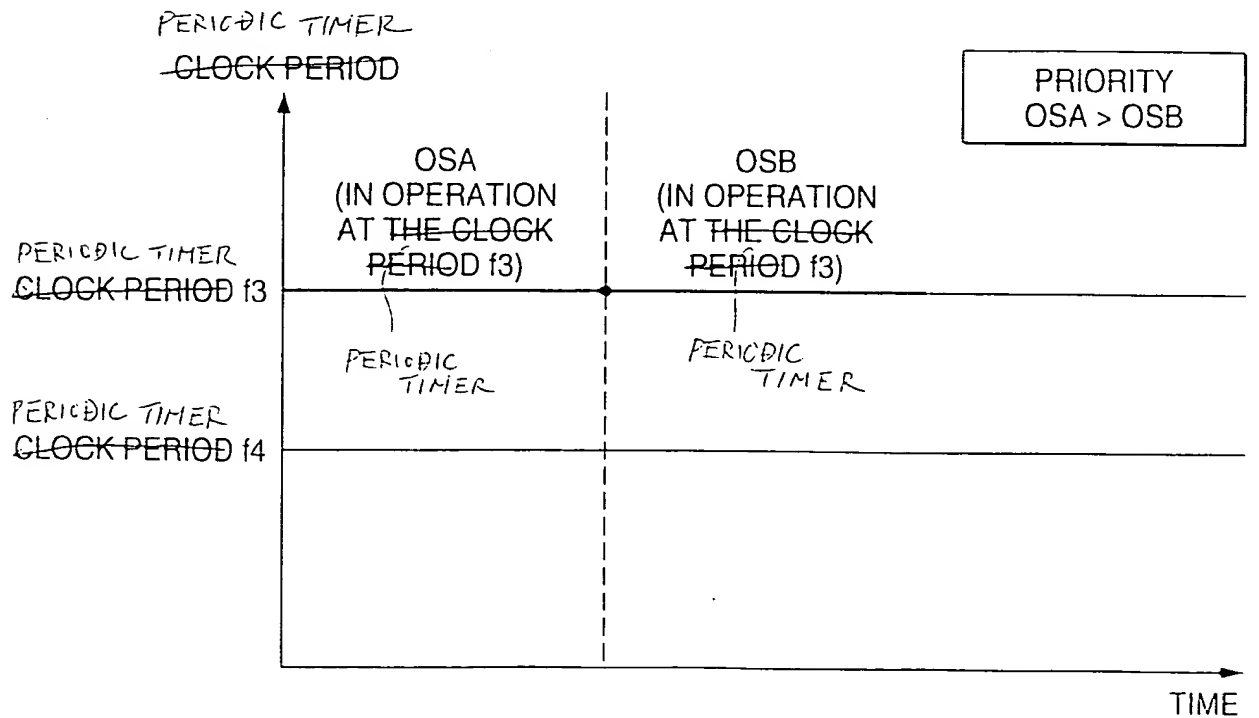


FIG. 29

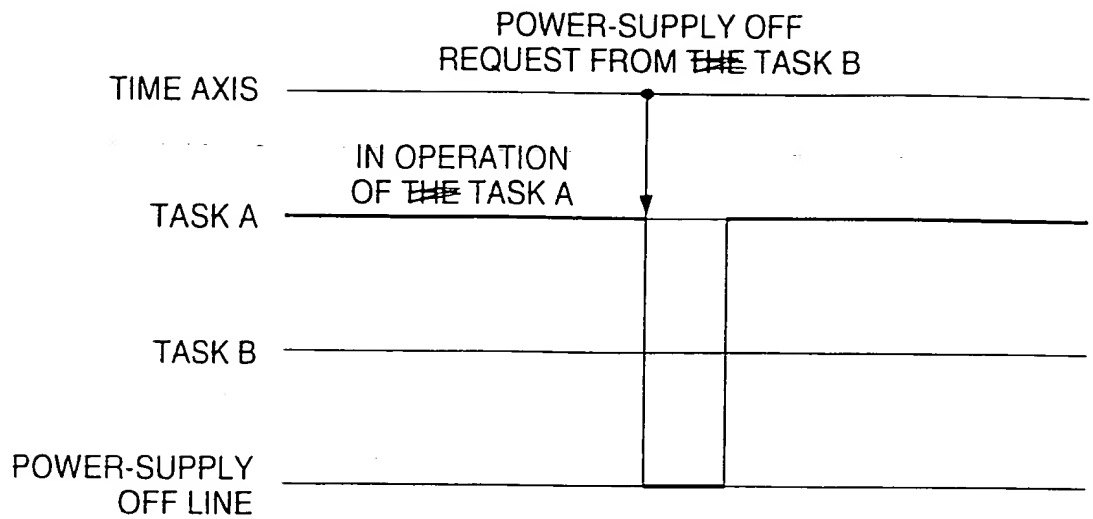


FIG. 30

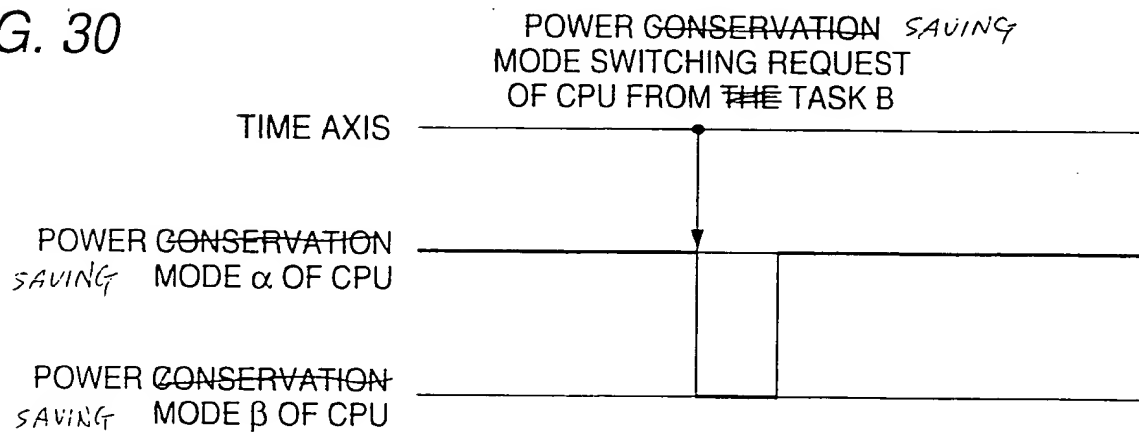


FIG. 31

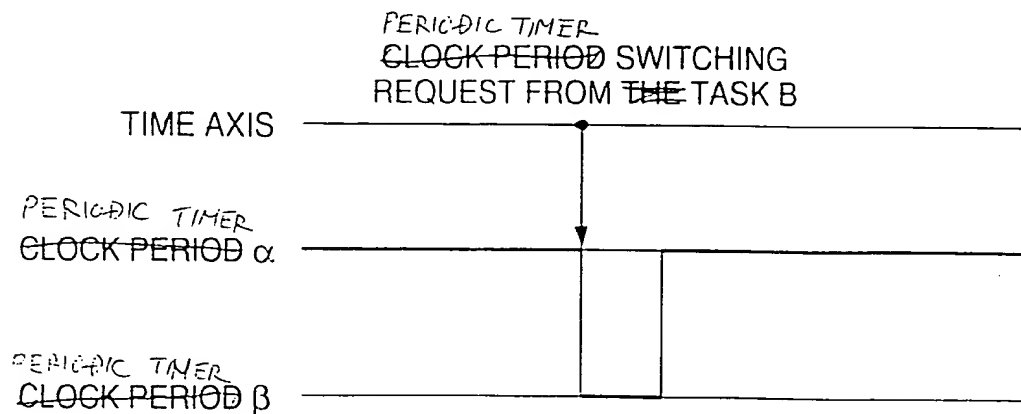


FIG. 32

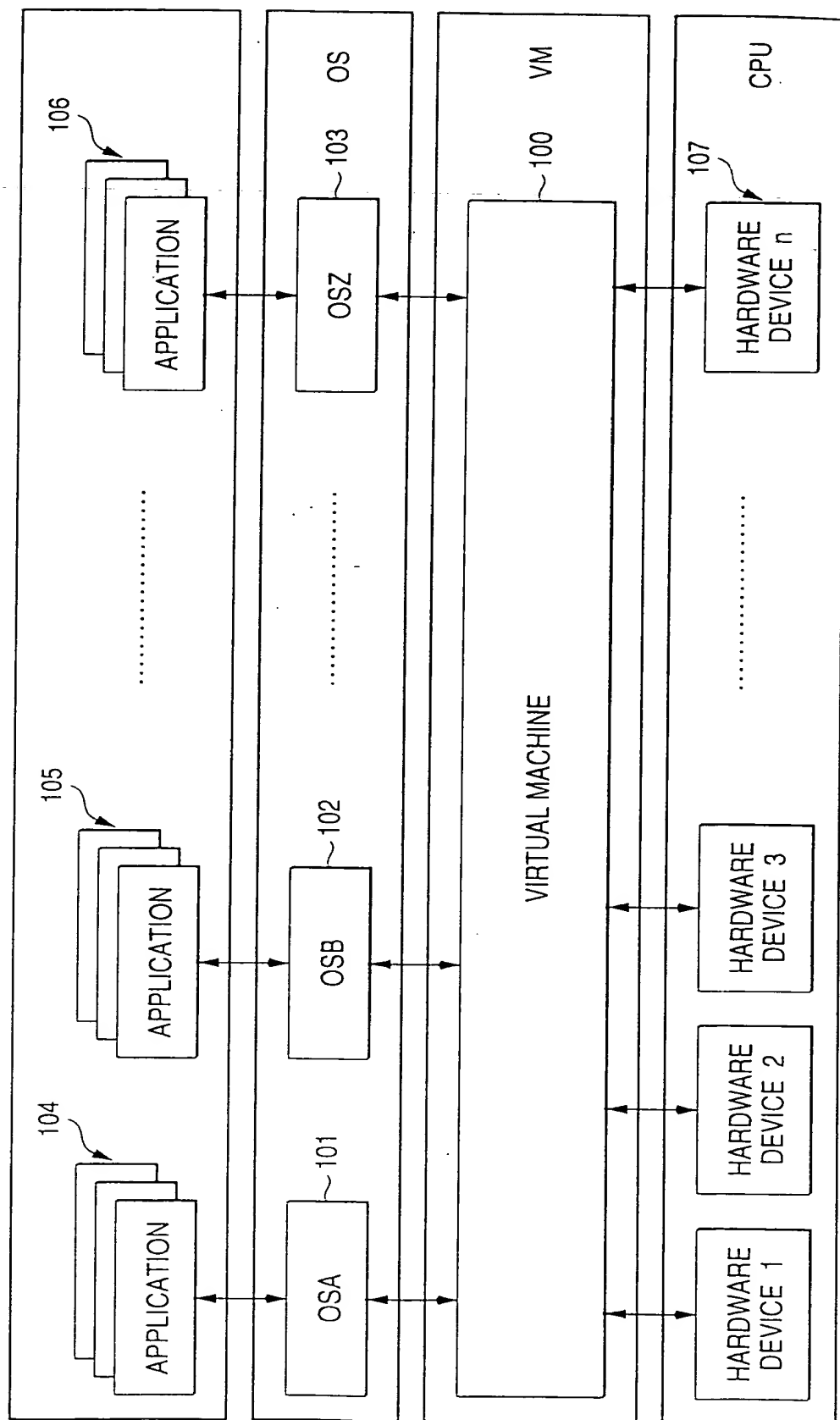


FIG. 33

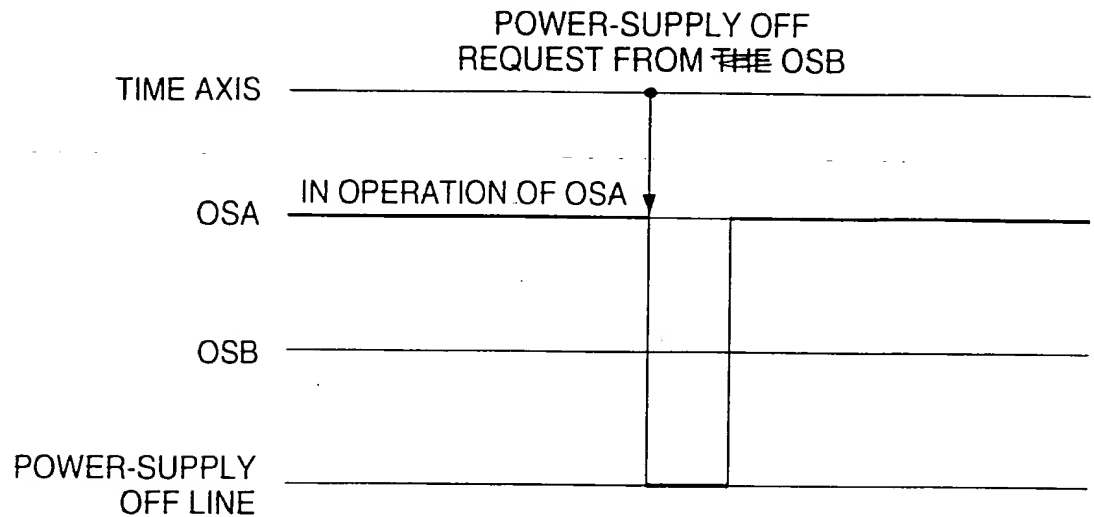


FIG. 34

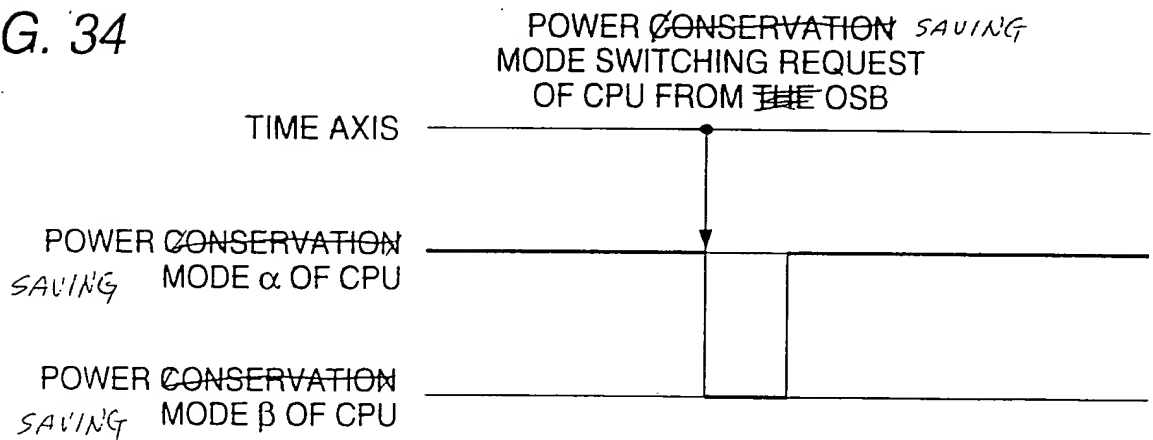


FIG. 35

